



Europäisches Patentamt

European Patent Office

Office européen des brevets

(11) Publication number:

0 049 521

A3

(12)

EUROPEAN PATENT APPLICATION

(21) Application number: 81107998.7

(51) Int. Cl.³: G 06 F 9/46

(22) Date of filing: 06.10.81

(30) Priority: 08.10.80 JP 139836/80

(72) Inventor: Kuwahara, Hiroshi
16-10, Omikacho-3-chome
Hitachi-shi(JP)

(43) Date of publication of application:
14.04.82 Bulletin 82/15

(72) Inventor: Ide, Jushi
473-44, Tonocho
Mito-shi(JP)

(88) Date of deferred publication of search report: 28.07.82

(72) Inventor: Hirai, Koji
2920-24, Mukaino
Mawatari Katsuta-shi(JP)

(84) Designated Contracting States:
DE GB

(72) Inventor: Kamiuchi, Toshiro
540-40, Josuiminamichō
Kodaira-shi(JP)

(71) Applicant: Hitachi, Ltd.
5-1, Marunouchi 1-chome
Chiyoda-ku Tokyo 100(JP)

(74) Representative: Ebbinghaus, Dieter et al.
K.L. Schiff Dr. A. von Funer Dipl.-Ing. P. Strehl Dr. U.
Schübel-Hopf Dipl.-Ing. D. Ebbinghaus Dr. Ing. D. Finck
Patentanwälte Mariahilfplatz 2 & 3
D-8000 München 90(DE)

(72) Inventor: Nakanishi, Hiroaki
16-26, Moriyamacho-1-chome
Hitachi-shi(JP)

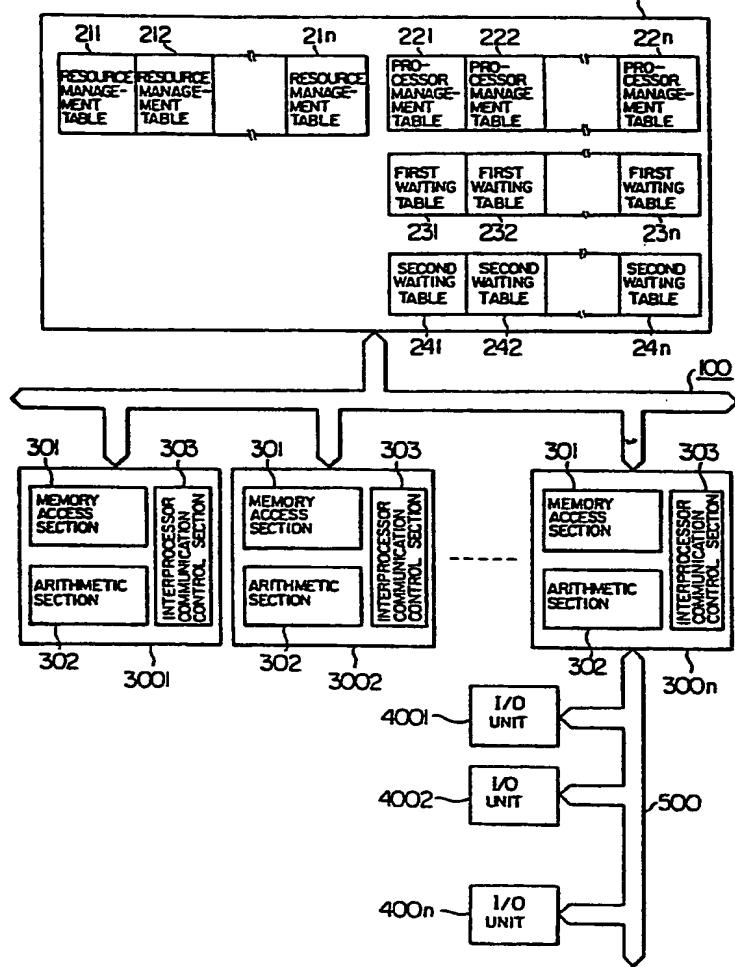
(54) Information processing system.

(57) An operating system processor (3001) for managing resources concentrically, job processors (3002, ..., 300n) for executing processes input/output processors (4001, 4002, ..., 400n) for executing input/output processes, a file processor for executing input/output processing to a file system, and a main memory (200) are connected together by a common bus (100). The operating system processor (3001) receives a resource request from the job processors (3002, ..., 300n) so as to manage the allotment of resources to the job processors by referring to a resource management table (211, 212, ..., 21n), a processor management table (221, 222, ..., 22n), and waiting tables (231, 232, ..., 23n, 241, 242, ..., 24n).

A3

EP 0 049 521

FIG. 1





European Patent
Office

EUROPEAN SEARCH REPORT

0049521

Application number

EP 81 10 7998

DOCUMENTS CONSIDERED TO BE RELEVANT		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. ?)
Category	Citation of document with indication, where appropriate, of relevant passages		
Y	IBM TECHNICAL DISCLOSURE BULLETIN vol.19, no 8, January 1977, NEW YORK USA; J.G.SAMS: "Low-cost systems using multiple processors", pages 2879-2883. *Page 2880, line 1 to page 2883, line 3*	1,2	G 06 F 9/46
Y	---	1	
Y	NEC RESEARCH and DEVELOPMENT no.36 January 1975, (TOKYO); KENI OHMORI et al.: "MICS-A MULTI-MICROPROCESSOR SYSTEM" pages 32-36. *Page 33, right-hand column, line 38 to page 35, left-hand column, line 15*	1,2	
Y	---	1,2	
Y	A.F.I.P.S. CONFERENCE PROCEEDINGS vol.40, SPRING JOINT COMPUTER CONFERENCE, NEW YORK USA, 1972; DAVIS et al.: "A building block approach to multiprocessing", pages 685-703. *Page 691 left-hand column, line 6 to page 703 left-hand column, line 32*	1,2	

The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
THE HAGUE	30-03-1982	LEPEE W.	
CATEGORY OF CITED DOCUMENTS			
X	particularly relevant if taken alone	T	theory or principle underlying the invention
Y	particularly relevant if combined with another document of the same category	E	earlier patent document, but published on, or after the filing date
A	technological disclosure	D	document cited in the application
O	non-written disclosure	L	document cited for other reasons
P	intermediate document	8	member of the same patent family, corresponding document